

000000 TTTTTTTTTT SSSSSSSS CCCCCCCC VV VV TTTTTTTTTT TTTTTTTTTT RRRRRRRR
000000 TTTTTTTTTT SSSSSSSS CCCCCCCC VV VV TTTTTTTTTT TTTTTTTTTT RRRRRRRR
00 00 TT SS CC VV VV TT TT TT RR RR
00 00 TT SS CC VV VV TT TT TT RR RR
00 00 TT SS CC VV VV TT TT TT RR RR
00 00 TT SS CC VV VV TT TT TT RR RR
00 00 TT SS CC VV VV TT TT TT RR RR
00 00 TT SS CC VV VV TT TT TT RR RR
00 00 TT SS CC VV VV TT TT TT RR RR
00 00 TT SS CC VV VV TT TT TT RR RR
00 00 TT SS CC VV VV TT TT TT RR RR
00 00 TT SS CC VV VV TT TT TT RR RR
00 00 TT SS CC VV VV TT TT TT RR RR
000000 TT SSSSSSSS CCCCCCCC VV VV TT TT TT RR RR
000000 TT SSSSSSSS CCCCCCCC VV VV TT TT TT RR RR
LL IIIIIII SSSSSSSS
LL IIIIIII SSSSSSSS
LL IIIIIII SS
LL IIIIIII SS
LL IIIIIII SS
LL IIIIIII SSSSSS
LL IIIIIII SSSSSS
LL IIIIIII SS
LL IIIIIII SS
LL IIIIIII SS
LLLLLLLLLL IIIIIII SSSSSSSS

(2)	47	HISTORY : Detailed Current Edit History
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```
0000 1 .TITLE OTSSCVTTR ; Convert text to real (D, G and H)
0000 2 .IDENT /1-011/ ; File: OTSCVTTR.MAR Edit: FM1011
0000 3 ****
0000 4 ****
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0000 23 *
0000 24 *
0000 25 ****
0000 26 *
0000 27 *
0000 28 * FACILITY: Language-independent support library
0000 29 ++
0000 30 * ABSTRACT:
0000 31 *
0000 32 * Performs conversion of character strings containing numbers to
0000 33 * floating datatypes. This routine supports FORTRAN F, E, D and
0000 34 * G format conversion, as well as similar types in other languages.
0000 35 *
0000 36 --
0000 37 *
0000 38 * VERSION: 1
0000 39 *
0000 40 * HISTORY:
0000 41 *
0000 42 * AUTHOR:
0000 43 * Steven B. Lionel, 2-Jul-79: Version 1
0000 44 *
0000 45 *
```

0000 47 .SBTTL HISTORY ; Detailed Current Edit History
0000 48
0000 49
0000 50 : EDIT HISTORY:
0000 51 :
0000 52 : 1-001 - Adapted from OTSSCVTTH version 1-003, changed to use Tom
0000 53 : 1-002 - Add forgotten FORSCNV IN_DEFG entry point. SBL 6-Jul-1979
0000 54 : 1-003 - Fix bug in SCALE. SBL 6-Jul-1979
0000 55 : 1-004 - Use Tom
0000 56 : Eggers' multi-precision multiply routine in OTSSCVTRT.
0000 57 : SBL 2-Jul-1979
0000 58 : 1-005 - Compensate for removal of STRING_LEN from convert frame.
0000 59 : SBL 11-Jul-79
0000 60 : 1-006 - Correct a typo in a comment. JBS 30-JUL-1979
0000 61 : 1-007 - Correct implementation of V_SKIPTABS. SBL 5-Sept-1979
0000 62 : 1-008 - Implement V_EXP LETTER. SBL 4-Dec-1979
0000 63 : 1-009 - Improve check for overflow, underflow to catch extreme cases.
0000 64 : Previously, extreme overflow could give invalid answer with
0000 65 : success status. SBL 17-June-1980
0000 66 : 1-010 - Speed up operations on FAC when it fits in a longword (9 or
0000 67 : fewer digits) or a quadword (18 or fewer digits). Improve
0000 68 : multiplication by 10, test for zero, and normalization. JAW
0000 69 : 28-Apr-1981
0000 70 : 1-011 - The OTSSSCVT_MUL now expects a simpler call interface. Namely
0000 71 : one does not have to find reciprocal of the desired entry in
0000 72 : OTSSSA_CVT_TAB to call this routine. Change the call to
0000 73 : OTSSSCVT_MUL to pass the address of the desired entry in
0000 74 : OTSSSA_CVT_TAB table instead of its reciprocal. FM 29-FEB-83

```

0000 76 .SBTTL DECLARATIONS
0000 77
0000 78 ; INCLUDE FILES:
0000 79 ; 80 ;
0000 81 ;
0000 82 ;
0000 83 ; EXTERNAL SYMBOLS:
0000 84 ;
0000 85 .DSABL GBL
0000 86 .EXTRN OTSS_INPCONERR ; Input conversion error
0000 87 .EXTRN OTSSSA_CVT_TAB ; Convert table address
0000 88 .EXTRN OTSSSCVT_MUL ; Conversion multiply routine
0000 89
0000 90 ;
0000 91 ; MACROS:
0000 92 ;
0000 93 ;
0000 94 ;
0000 95 ; PSECT DECLARATIONS:
0000 96 ;
0000 97 00000000 .PSECT _OTSSCODE PIC, SHR, LONG, EXE, NOWRT
0000 98
0000 99
0000 100 ;
0000 101 ; EQUATED SYMBOLS:
0000 102 ;
0000 103 ;
0000 104 000003FC 105 REGMASK = ^M<R2, R3, R4, R5, R6, R7, R8, R9>
0000 106 ; register save mask
0000 107 ; Note: integer overflow not enabled
0000 108
0000 109 ;+
0000 110 ; The following symbols are used to indicate the bit position of the flag
0000 111 ; register.
0000 112 ;-
0000 113 0000001F 114 V_NEGATIVE = 31 ; flag bit: 1 if negative sign
0000001E 115 V_DEC_POINT = 30 ; flag bit: 1 if decimal point is seen
40000000 0000 116 M_DEC_POINT = 1@30 ; mask for V_DEC_POINT
0000001D 0000 117 V_NEG_DECEXP = 29 ; flag bit: 1 if exponent has negative sign
20000000 0000 118 M_NEG_DECEXP = 1@29 ; mask for V_NEG_DECEXP
0000001C 0000 119 V_DECEXP = 28 ; flag bit: 1 if exponent field exist
10000000 0000 120 M_DECEXP = 1@28 ; mask for V_DECEXP
0000001B 0000 121 V_EXT_BITS = 27 ; flag bit: 1 if extension bits
08000000 0000 122 M_EXT_BITS = 1@27 ; wanted
0000 123 ; mask for V_EXT_BITS
0000 124
0000 125
0000 126 ;+
0000 127 ; Literals for data types
0000 128 ;-
00000000 0000 129 K_DTYPE_D = 0 ; D-floating
00000001 0000 130 K_DTYPE_G = 1 ; G-floating
00000002 0000 131 K_DTYPE_H = 2 ; H-floating
0000 132

```

0000 133 ;+
0000 134 ; Temporary stack offsets
0000 135 ;-
0000 136
00000000 0000 137 TEMP = 0 ; temporary storage during
00000004 0000 138 8 word shift
00000008 0000 139 FLAG = 4 ; flag storage
00000008 0000 140 was R6 in FORSCNV, IN, DEFG
0000000C 0000 141 DIGITS = 8 ; digits to right of decimal
0000000C 0000 142 point (was R7)
00000010 0000 143 DECEXP = 12 ; decimal exponent
00000010 0000 144 DTTYPE = 16 ; Datatype code
0000 145
0000 146 ;+
0000 147 ; Stack offsets for OTSSCVT_MUL routine
0000 148 ;-
00000014 0000 149 BINNUM = 20 ; Binary fraction storage
00000024 0000 150 INT = 36 ; Overflow area for BINNUM
00000028 0000 151 BINEXP = 40 ; Binary exponent
0000002C 0000 152 PRODF_4 = 44 ; Multiply temporary
00000030 0000 153 PRODF_4 = 48 ; Multiply temporary
00000040 0000 154 CRY = 64 ; Carry save area
00000050 0000 155 FRAME = CRY + 16 ; Stack frame size
0000 156
0000 157 ;+
0000 158 ; Constants
0000 159 ;-
0CCCCCCC 0000 160
0CCCCCCC 0000 161 L_2P31_DIV_10 = 214748364 ; (2**31)/10
0CCCCCCC 0000 162

0000 164 .SBTTL OTSSCVT_T_x - convert text to floating
0000 165
0000 166 :++
0000 167 : FUNCTIONAL DESCRIPTION:
0000 168
0000 169 OTSSCVT_T_x converts a text string containing a representation
0000 170 of a numeric value to a floating representation of that
0000 171 value. The routine supports FORTRAN F,E,D and G input type
0000 172 conversion as well as similar types for other languages.
0000 173
0000 174 The description of the text representation converted by
0000 175 OTSSCVT_T_x is as follows:
0000 176
0000 177 <0 or more blanks>
0000 178 <"+", "-" or nothing>
0000 179 <0 or more decimal digits>
0000 180 <"." or nothing>
0000 181 <0 or more decimal digits>
0000 182 <exponent or nothing, where exponent is:
0000 183 < <"E", "e", "D", "d", "Q", "q">
0000 184 <0 or more blanks>
0000 185 <"+", "-" or nothing>>
0000 186 or
0000 187 <"+" or "-">>
0000 188 <0 or more decimal digits>>
0000 189 <end of string>
0000 190
0000 191 Notes: 1. Unless "caller_flags" bit V_SKIPBLANKS
0000 192 is set, blanks are equivalent to
0000 193 decimal "0". If V_SKIPBLANKS is set,
0000 194 blanks are always ignored.
0000 195 2. There is no difference in semantics
0000 196 between any of the 6 valid exponent
0000 197 letters.
0000 198 3. If "caller_flags" bit V_ONLY_E is set,
0000 199 the only valid exponent letters are
0000 200 "E" and "e"; any others will be treated
0000 201 as an invalid character.
0000 202 4. If "caller_flags" bit V_SKIPTABS is set,
0000 203 tab characters are ignored else they are
0000 204 an error.
0000 205 5. If "caller_flags" bit V_EXP LETTER is set,
0000 206 the exponent, if present, must start with
0000 207 a valid exponent letter i.e. 1.2E32.
0000 208 If clear, the exponent letter may be omitted.
0000 209 i.e. 1.2+32.
0000 210
0000 211
0000 212 : CALLING SEQUENCE:
0000 213
0000 214 status.wlc.v = OTSSCVT_T_x (in_str.rt.dx, value.wfx.r
0000 215 [, digits_in_fract.rlu.v
0000 216 [, scale_factor.rl.v
0000 217 [, caller_flags.rlu.v,
0000 218 [, ext_bits.wx.r]]])
0000 219
0000 220 : where "x" is the datatype of the floating value, either

```

0000 221 : D, G or H.
0000 222 :
0000 223 :
0000 224 : INPUT PARAMETERS:
0000 225 :
0000 226 in_str = 4 ; input string descriptor by
0000 227 : reference.
0000 228 digits_in_fract = 12 ; If no decimal point is
0000 229 : present in input, specifies
0000 230 : how many digits are to be
0000 231 : treated as being to the
0000 232 : right of the decimal point.
0000 233 : If omitted, 0 is the default.
0000 234 scale_factor = 16 ; signed scale factor. If
0000 235 : present, and exponent absent,
0000 236 : the result value is
0000 237 : multiplied by 10**factor.
0000 238 : If "caller_flags" bit
0000 239 : V_FORCESCALE is on, the
0000 240 : scale factor is always applied.
0000 241 caller_flags = 20 ; flags supplied by caller
0000 242 :
0000 243 ;+ Definitions of caller supplied flags
0000 244 ;-
0000 245 :
0000 246 V_SKIPBLANKS = 0 ; If set, blanks are ignored
0000 247 V_ONLY_E = 1 ; If set, only E or e exponents
0000 248 : allowed (BASIC+2, PL/I)
0000 249 V_ERR_UFLD = 2 ; If set, error on underflow
0000 250 V_DONTROUND = 3 ; If set, don't round value
0000 251 M_DONTROUND = 103 ; Mask for V_DONTROUND
0000 252 V_SKIPTABS = 4 ; If set, tabs are ignored.
0000 253 : If clear, tabs are illegal.
0000 254 V_EXP_LETTER = 5 ; If set, an exponent must begin
0000 255 : with a valid exponent letter.
0000 256 : If clear, the exponent letter
0000 257 : may be omitted.
0000 258 V_FORCESCALE = 6 ; If set, the scale factor is
0000 259 : always applied. If clear, it
0000 260 : is only applied if there is
0000 261 : no exponent present in the
0000 262 : string.
0000 263 :
0000 264 NO_OF_FLAGS = 7 ; Number of flags
0000 265 :
0000 266 :
0000 267 : IMPLICIT INPUTS:
0000 268 :
0000 269 :
0000 270 : NONE
0000 271 :
0000 272 : OUTPUT PARAMETERS:
0000 273 :
0000 274 value = 8 ; floating result by ref
0000 275 ext_bits = 24 ; If present, the value will
0000 276 : NOT be rounded and the first
0000 277 : n bits after truncation will

```

0000 278 ; be returned in this argument.
 0000 279 ; For D-floating, the next 8 bits
 0000 280 ; are returned as a byte.
 0000 281 ; For G and H floating, 11 and 15
 0000 282 ; bits are returned, respectively,
 0000 283 ; as a word, left-adjusted.
 0000 284 ; These values are suitable for
 0000 285 ; use as the extension operand
 0000 286 ; in an EMOD instruction.
 0000 287 ; WARNING: The bits returned for
 0000 288 ; H-floating may not be precise,
 0000 289 ; due to the fact that calculations
 0000 290 ; are only carried to 128 bits.
 0000 291 ; However, the error should be
 0000 292 ; small. D and G datatypes
 0000 293 ; return guaranteed exact bits,
 0000 294 ; but they are not rounded.
 0000 295 ;
 0000 296 ; IMPLICIT OUTPUTS:
 0000 297 ;
 0000 298 ; NONE
 0000 299 ;
 0000 300 ; COMPLETION CODES:
 0000 301 ;
 0000 302 ;
 0000 303 ; OTSS_INPCONERR - Error if illegal character in input or
 0000 304 ; overflow.
 0000 305 ; SSS_NORMAL - success
 0000 306 ;
 0000 307 ; SIDE EFFECTS:
 0000 308 ;
 0000 309 ; NONE
 0000 310 ;
 0000 311 ;--
 0000 312 ;
 0000 313 ;
 0000 314 ;
 03FC 0000 315 .ENTRY OTSSCVT_T_H, REGMASK ; entry for OTSSCVT_T_H
 SE 00000050 8F C2 0002 316 SUBL2 #FRAME, SP ; Create stack frame
 10 AE 02 D0 0009 317 MOVL #K_DTYPE_H, DTYPE(SP) ; Set datatype code
 1C 11 000D 318 BRB COMMON ; Go to common code
 000F 319 ;
 03FC 000F 320 .ENTRY OTSSCVT_T_G, REGMASK ; entry for OTSSCVT_T_G
 SE 00000050 8F C2 0011 321 SUBL2 #FRAME, SP ; Create stack frame
 10 AE 01 D0 0018 322 MOVL #K_DTYPE_G, DTYPE(SP) ; Set datatype code
 0D 11 001C 323 BRB COMMON ; Go to common code
 001E 324 ;
 001E 325 ;
 001E 326 ;
 03FC 001E 327 FOR\$CNV_IN DEFG:: .ENTRY OTSSCVT_T_D, REGMASK ; Create stack frame
 SE 00000050 8F C2 0020 328 SUBL2 #FRAME, SP ; Set datatype code
 10 AE 00 D0 0027 329 MOVL #K_DTYPE_D, DTYPE(SP) ; Go to common code
 002B 330 ;
 002B 331 ;
 002B 332 ;
 002B 333 ;+ Register usage and abbreviations:
 002B 334 ;

002B 335 :
 002B 336 :
 002B 337 :
 002B 338 :
 002B 339 :
 002B 340 :
 002B 341 :
 002B 342 :
 002B 343 :
 002B 344 :
 002B 345 :
 002B 346 :
 002B 347 :
 002B 348 :
 002B 349 :
 04 AE D4 002B 350 :
 05 6C 91 002E 351 :
 0031 352 :
 0031 353 :
 0033 354 :
 003A 355 :
 003A 356 :
 003D 357 :
 08 1F 003D 358 :
 003F 359 :
 0047 360 :
 0047 361 :
 0047 362 5\$:
 004B 363 :
 004B 364 :
 004B 365 :
 52 D4 004B 366 :
 54 7C 004D 367 :
 56 7C 004F 368 :
 08 AE D4 0051 369 :
 03 6C 91 0054 370 :
 0057 371 :
 05 1F 0057 372 :
 0059 373 :
 58 7C 005E 374 10\$:
 0060 375 :
 335 :
 336 :
 337 :
 338 :
 339 :
 340 :
 341 :
 342 :
 343 :
 344 :
 345 :
 346 :
 347 :
 348 :
 349 :
 002B 350 :
 002E 351 :
 0031 352 :
 0033 353 :
 003A 354 :
 003D 355 :
 003A 356 :
 003D 357 :
 08 1F 003D 358 :
 003F 359 :
 0047 360 :
 0047 361 :
 004B 362 5\$:
 004B 363 :
 004B 364 :
 004B 365 :
 52 D4 004B 366 :
 54 7C 004D 367 :
 56 7C 004F 368 :
 08 AE D4 0051 369 :
 03 6C 91 0054 370 :
 0057 371 :
 05 1F 0057 372 :
 0059 373 :
 58 7C 005E 374 10\$:
 0060 375 :
 R0 - Generally count of input characters remaining.
 R1 - Generally pointer to input character.
 R2 - Generally holds decimal exponent.
 R3 - Used first to hold current character, then as extra precision bits for the fraction.
 R4-R7 - The 128 bit binary fraction.
 R8 - Count of digits seen after overflow.
 R9 - Count of significant digits seen in fraction (number of digits currently held in R4:R7).
 FAC: Binary fraction, R4-R7.
 COMMON:
 CLRL FLAG(SP) : clear flags
 CMPB (AP), #<caller_flags/4> : is optional caller_flags argument present?
 if not, skip
 BLSSU 5\$:
 INSV caller_flags(AP), #0, #NO_OF_FLAGS, FLAG(SP)
 CMPB (AP), #<ext_bits/4> : set caller flags
 is optional ext_bits argument present?
 if not, skip
 BLSSU 5\$:
 BISL #<M_EXT_BITS+M_DONTROUND>, FLAG(SP) : set bit indicating it is there plus dont round bit
 R0 will get string length, the CLASS and TYPE fields will go away after the first SKPC.
 R1 points to input string.
 R2 = DECIMAL_EXPONENT = 0
 R4-R7 = FAC \equiv 0
 MOVQ a_in_str(AP), R0 :
 CLRL R2 : digits in fraction
 CLRQ R4 :
 CLRQ R6 :
 CLRL DIGITS(SP) :
 CMPB (AP), #<digits_in_fract/4> : is digits_in_fract present?
 BLSSU 10\$: skip if not
 MOVL digits_in_fract(AP), DIGITS(SP) : set if present
 CLRQ R8 : (clear digit counts (R8 & R9).

0060 377 ;+
 0060 378 ; Find first non-blank. If none, return zero. Otherwise process
 0060 379 ; character.
 0060 380 ;-
 0060 381 ;
 61 50 20 38 0060 382 20\$: SKPC #^A/ /, R0, (R1) ; skip blanks
 0064 383 ; R0 = #CHAR REMAINING
 0064 384 ; R1 = POINTER_TO_INPUT
 0064 385 ; Z bit is set if all blanks
 03 14 0064 386 ; non-blank found?
 011C 31 0066 387 30\$: BGTR 30\$; if not, return zero
 53 61 9A 0069 388 BRW ZERO ; R3 = ASCII(current char)
 0D 04 04 E1 006C 389 MOVZBL (R1), R3 ; ; Not skipping tabs?
 09 53 D1 0071 390 BBC #V_SKIPTABS, FLAG(SP), 35\$; Is character a tab?
 08 12 0074 391 CMPL R3 #9 ; No
 51 D6 0076 392 BNEQ 35\$; Yes, bump pointer
 E5 50 F5 0078 393 INCL R1 ; Decrement character count
 0107 31 007B 394 SOBGTR R0, 20\$; Value is zero
 20 53 91 007E 395 35\$: CMPB R3 #^A/-/ ; Is current char a "-" sign?
 05 12 0081 396 BNEQ 40\$; branch if not
 15 04 AE 1F E3 0083 397 BBCS #V_NEGATIVE, FLAG(SP), DIGIT_LOOP ; set negative flag and continue
 28 53 91 0088 398 40\$: CMPB R3, #^A/+/ ; is current char a "+" sign?
 10 13 008B 400 BEQL DIGIT_LOOP ; yes, ignore and continue
 2E 53 91 008D 401 CMPB R3, #^A/./ ; is current char a "."?
 15 12 0090 402 BNEQ CHECK_DIGIT ; no, should be a digit
 04 AE 40000000 8F C8 0092 403 BISL #M_DEC_POINT, FLAG(SP) ; set decimal point encountered
 08 AE D4 009A 404 CLRL DIGITS(SP) ; ignore digits_in_fract
 009D 405

		009D	407	:+	
		009D	408	: Collect integer and fraction digits. Blanks are zeroes unless	
		009D	409	: V_SKIPBLANKS is set in which case they are ignored.	
		009D	410	: Tabs are illegal unless V_SKIPTABS is on in which case they are ignored.	
		009D	411	:-	
		009D	412		
		009D	413	DIGIT_LOOP:	
032B	30	009D	414	BSBW	RGET
50	D5	00A0	415	TSTL	R0
03	14	00A2	416	BGTR	CHECK_DIGIT
00DA	31	00A4	417	BRW	SCALE
53	30	C2	418	CHECK_DIGIT:	
09	53	D1	419	SUBL	#^A/0/, R3
1A	1A	00AD	420	CMPL	R3 #9
0CCCCCCC 8F	57	D1	421	BGTRU	NOf_DIGIT
		00AF	422	CMPL	R7, #L_2P31_DIV_10
		00B6	423		
		00B6	424		
		00B6	425	BLEQU	10\$
		00B8	426	INCL	R8
		00BA	427	BRB	2\$
D9 04 AE	032D	30	428	10\$:	BSBW
	1E	E1	429	2\$:	BBC
		00C4	430	#V_DEC_POINT, FLAG(SP), DIGIT_LOOP	
		00C4	431		: check to see if decimal
		00C4	432		: point has been seen
08 AE	D6	00C4	433	INCL	- continue if not.
D4	11	00C7	434	BRB	: bump DIGITS
		00C9	435	DIGITS(SP)	
					: branch back to read more

			00C9	437	:+		
			00C9	438	:- A non-digit has been found. Check for sign or exponent letter.		
			00C9	439	-		
			00C9	440			
			00C9	441	NOT_DIGIT:		
FFFFFE 8F	53	D1	00C9	442	CMPL	R3, #<^A/./-^A/0/	: check if current char is a ".
	4C	13	00D0	443	BEQL	DECIMAL_POINT	branch to DECIMAL_POINT if yes
FFFFFB 8F	53	D1	00D2	444	CMPL	R3, #<^A/+/-^A/0/	: '+'?
	53	13	00D9	445	BEQL	EXP_PLUS	Exponent starts with plus
FFFFFD 8F	53	D1	00D8	446	CMPL	R3, #<^A/-/-^A/0/	: '-'?
	53	13	00E2	447	BEQL	EXP_MINUS	Exponent starts with a minus
15	53	D1	00E4	448	CMPL	R3, #<^A/E/-^A/0/	: 'E'?
	40	13	00E7	449	BEQL	EXPON	process exponent
35	53	D1	00E9	450	CMPL	R3, #<^A/e/-^A/0/	: 'e'?
	58	13	00EC	451	BEQL	EXPON	process exponent
18 04 AE	01	E0	00EE	452	BBS	#V_ONLY_E, FLAG(SP), 10\$: ERROR
	14	53	D1	00F3	453		: error if only E, e allowed
	31	13	00F6	454	CMPL	R3, #<^A/D/-^A/0/	: 'D'?
34	53	D1	00F8	455	BEQL	EXPON	process exponent
	2C	13	00FB	456	CMPL	R3, #<^A/d/-^A/0/	: 'd'?
21	53	D1	00FD	457	BEQL	EXPON	process exponent
	27	13	0100	458	CMPL	R3, #<^A/Q/-^A/0/	: 'Q'?
00000041 8F	53	D1	0102	459	BEQL	EXPON	process exponent
	1E	13	0109	460	CMPL	R3, #<^A/q/-^A/0/	: 'q'?
	0089	31	0108	461	BEQL	EXPON	process exponent
			010E	462	10\$:	BRW	: error since illegal char.
			010E	463			
			010E	464	:+		
			010E	465	:- The exponent did not start with a letter. This is not allowed		
			010E	466	:- if V_EXP LETTER is set.		
			010E	467	-		
			010E	468	EXP_PLUS:		
41 04 AE	05	E1	010E	469	BBC	#V EXP LETTER, FLAG(SP), EXP LOOP	
0081	31	0113		470	BRW	ERROR ; Not allowed	
7C 04 AE	05	E0	0116	471	EXP_MINUS:		
002E	31	C11B		472	BBS	#V EXP LETTER, FLAG(SP), ERROR	
			011E	473	BRW	EXP_NEG ; Ok	
			011E	474	:+		
			011E	475	:- Decimal point has been found		
			011E	476	-		
			011E	477			
			011E	478	DECIMAL_POINT:		
74 04 AE	1E	E2	011E	479	BBSS	#V DEC POINT, FLAG(SP), ERROR ; error if duplicate	
08 AE	D4	0123		480	CLRL	DIGIT\$TSP) ; reset DIGITS	
FF74	31	0126		481	BRW	DIGIT_LOOP ; get fraction digits	

0129 483 :+
 0129 484 : Loop to collect digits, store the accumulated DECIMAL_EXPONENT in R2
 0129 485 :-
 0129 486 :
 0129 487 EXPON:
 50 D7 0129 488 DECL R0
 44 15 012B 489 BLEQ EXP_DONE
 51 D6 012D 490 INCL R1
 20 38 012F 491 SKPC #^A/ /, R0, (R1)
 5C 15 0132 492 BLEQ EXP_DONE
 05 04 AE 06 0138 493 MOVZBL (R1), R3
 09 53 61 9A 0132 494 BBC #V_SKIPTABS, FLAG(SP), 108 ; Not skipping tabs?
 53 04 E1 0138 495 CMPL R3, #9
 06 53 D1 013D 496 BEQL EXPON
 2B E7 13 0140 497 108: CMPL R3, #^A/+/+
 0D 0D 13 0145 498 BEQL EXP_LOOP
 2D 53 D1 0147 499 CMPL R3, #^A/-/
 0F 12 014A 500 BNEQ EXP_CHECK
 04 AE 20000000 BF C8 014C 501 EXP_NEG:
 0274 30 0154 502 BISL #M_NEG_DECEXP, FLAG(SP) ; exponent is negative
 50 D5 0157 503 EXP_LOOP:
 16 15 0159 504 BSBW RGET
 0158 505 TSTL R0
 53 30 C2 0158 506 BLEQ EXP_DONE
 37 19 015E 507 EXP_CHECK:
 09 53 D1 0160 508 SUBL #^A/0/, R3
 32 1A 0163 509 BLSS ERROR
 52 0A C4 0165 510 CMPL R3, #9
 2D 1D 0168 511 BGTRU ERROR
 52 53 C0 016A 512 MULL #10, R2
 28 1D 016D 513 BVS ERROR
 E3 11 016F 514 ADDL R3, R2
 0171 515 BVS ERROR
 0171 516 BRB EXP_LOOP
 03 04 AE 1D E1 0171 517 EXP_DONE:
 52 52 CE 0176 518 BBC #V_NEG_DECEXP, FLAG(SP), 18 ; check for negative
 04 AE 10000000 BF C8 0179 519 MNEGL R2, R2 ; negate DECIMAL_EXPONENT
 0181 520 BISL #M_DECEXP, FLAG(SP) ; exponent field exists
 0181 521 18: 522
 0181 523

```

0181 525 :+
0181 526 : Done collecting input characters for digits and/or exponent
0181 527 : If FAC=0, no scaling is necessary, just store 0.0 and return.
0181 528 :
0181 529 :
0181 530 SCALE:
59 05 0181 531 TSTL R9
1B 12 0183 532 BNEQ INIT_BINEXP
0185 533 : Check FAC for zero.
0185 534 : Branch if not.
0185 535 : Value is zero.
0185 536 :
0185 537 ZERO:
50 01 00 0185 538 MOVL #1, R0
0188 539 ZERO_VALUE: ; SSS_NORMAL
51 08 AC 00 0188 540 MOVL value(AP), R1
02 10 AE 91 018C 541 CMPB DTTYPE(SP), #K_DTTYPE_H
02 19 0190 542 BLSS 10S
01 7C 0192 543 CLRQ (R1)+ ; Check length of datatype
61 7C 0194 544 10S: CLRQ (R1)
04 0196 545 RET ; return with status in R0
0197 546
0197 547 :
0197 548 :+
0197 549 : ERROR return
0197 550 :
0197 551 :
50 00000000'8F 0197 552 ERROR:
E8 11 019E 553 MOVL #OTSS_INPERR, R0 ; R0 = error return code
01A0 554 BRB ZERO_VALUE ; Set value to zero and exit
01A0 555 :
01A0 556 :+
01A0 557 : Set R1 to the binary exponent [exponent bias + 128 - 1].
01A0 558 : 128 is number of fraction bits and 1 is
01A0 559 : for the MSB fraction bit which will be hidden later.
01A0 560 : BINARY_EXPONENT will be modified during normalization process.
01A0 561 :
01A0 562 :
01A0 563 INIT_BINEXP:
02 00 10 AE 8F 01A0 564 CASEB DTTYPE(SP), #K_DTTYPE_D, #K_DTTYPE_H ; Select on datatype
0006' 01A5 565 1$: .WORD D_EXP-1$ ; D-Floating
000D' 01A7 566 .WORD G_EXP-1$ ; G-Floating
0014' 01A9 567 .WORD H_EXP-1$ ; H-Floating
51 00FF 8F 3C 01AB 568 D_EXP: MOVZWL #2^X80+^X7F>, R1 ; D-Floating
0C 11 01B0 569 BRB EXP_COMMON
51 047F 8F 3C 01B2 570 G_EXP: MOVZUL #C^X400+^X7F>, R1 ; G-Floating
05 11 01B7 571 BRB EXP_COMMON
51 407F 8F 3C 01B9 572 H_EXP: MOVZWL #C^X4000+^X7F>, R1 ; H-Floating
01BE 573 BRB EXP_COMMON
01BE 574 :
01BE 575 :+
01BE 576 : Find the true decimal exponent for the value expressed in FAC.
01BE 577 : True decimal exponent = Explicit exponent - [scale factor] -
01BE 578 : digits in fraction + number of overflows
01BE 579 :
01BE 580 :
01BE 581 EXP_COMMON:

```

50	52	00	01BE	582	MOVL	R2, R0	: R0 = DECIMAL EXPONENT
04	6C	91	01C1	583	CMPB	(AP), #<scale_factor/4>	: is scale_factor present
05 04 AE	06	E0	01C4	584	BLSSU	208	: no
04 04 AE	1C	E0	01C6	585	BBS	#V_FORCESCALE, FLAG(SP)	: 108 ; force scaling
			01CB	586	BBS	#V_DECEXP, FLAG(SP), 208	: ignore factor if exponent
58	10 AC	C2	01D0	587	108:	SUBL	: exists
			01D4	588		scale_factor(AP), R8	: adjust decimal exponent for
58	08 AE	C2	01D4	589	208:	SUBL	: scale factor
			01D8	590		DIGITS(SP), R8	: adjust for digits in fraction
0C AE	50	58	C1	592	ADDL3	R8, R0, DECEXP(SP)	: adjust decimal exponent for overflow
	B8	1D	01DD	593	BVS	ERROR	: If overflow, error
			01DF	594			

09	59	D1	01DF	596	:+	Normalization. Shift the value left until bit 31 of R7 is on.	
12	59	D1	01E2	597	:-	Adjust the binary exponent appropriately.	
	1A	15	01E4	598			
			01E7	599			
			01E9	600			
			01E9	601	CMPL	R9, #9	
			01E9	602	BLEQ	N1	: Are there more than 9 digits?
			01E9	603	CMPL	R9, #18	: If not, use N1.
			01E9	604	BLEQ	N2	: Are there more than 18 digits?
			01E9	605			: If not, use N2.
			01E9	606	:+	Process all four longwords, since there are more than 18 digits.	
			01E9	607	:-		
6E	40	57	1F	608	N4:	BBS #31, R7, REBASE	: Quit when R7<31> = 1.
	55	01	1F	609		EXTZV #31, #1, R5, TEMP(SP)	: Save bit lost in shift.
	54	54	01	610		ASHQ #1, R4, R4	: Shift low part by one bit.
56	56	01	79	611		ASHQ #1, R6, R6	: Shift high part by one bit.
	01	00	6E	612		INSV TEMP(SP), #0, #1, R6	: Replace bit lost in shift.
			51	613		DECL R1	: Adjust exponent by one.
			E6	614		BRB N4	: Go back and retest.
			0203	615	:+	Process two low-order longwords only, since there are <= 18 digits.	
			0203	616	:-		
51	00000040	8F	C2	617	N2:	SUBL #64, R1	: Adjust exponent by 64.
	56	54	7D	618		MOVQ R4, R6	: "Shift" by 64 bits.
56	56	01	D7	619	10\$:	DECL R1	: Adjust exponent by one.
	F8	18	79	620		ASHQ #1, R6, R6	: Shift one bit.
	54	7C	020F	621		BGEQ 10\$: If R7<31> = 0, repeat.
	14	11	0213	622		CLRQ R4	: Clear low-order 64 bits.
			0215	623		BRB REBASE	: Continue with next phase.
			0217	624	:+	Process only the low-order longword, since there are <= 9 digits.	
			0219	625	:-		
51	00000060	8F	C2	626	N1:	SUBL #96, R1	: Adjust exponent by 96.
	57	54	00	627		MOVL R4, R7	: "Shift" by 96 bits.
57	57	01	D7	628	20\$:	DECL R1	: Adjust exponent.
	F8	18	78	629		ASHL #1, R7, R7	: Shift one bit.
	54	D4	0225	630		BGEQ 20\$: If R7<31> = 0, repeat.
			0229	631		CLRL R4	: Clear low-order longword.
			0228	632	:+	Rebasing. R4-R7 now contains a binary fraction normalized with	
			022D	633		the radix point to the left of bit 31 of R7. R1 contains the	
			022D	634		current binary exponent and DECEXP(SP) contains the current decimal	
			022D	635		exponent.	
			022D	636	:+	Therefore, the number can be represented as:	
			022D	637		$2^{b} * \text{fraction} * 10^{d}$	
			022D	638		where b is the binary exponent and d is the decimal exponent. We	
			022D	639		call OTSSSCVT_MUL to multiply the number by some power of 10 such	
			022D	640		that d goes to zero and b goes to the appropriate value. When d is	
			022D	641		zero, b contains the proper binary exponent.	
			022D	642		:-	
			022D	643			
			022D	644			
			022D	645			
			022D	646			
			022D	647			
			022D	648			
			022D	649			
58	14	AE	9E	650	REBASE:	MOVAB BINNUM(SP), R8	: R8 is used by subroutine as base
28	AE	51	00	651		MOVL R1, BINEXP(SP)	: Store binary exponent
14	AE	54	7D	652		MOVQ R4, BINNUM+0(SP)	: Store fraction

1C AE 56 7D 0239 653	MOVQ R6, BINNUM+8(SP)	
57 0D 00 0230 654	MOVL #13, R7	: Highest bit number possibly
50 52 0C 14 00 0240 655	MOVL #20, R2	: on in decimal exponent.
40 AE 00 0243 656	MOVL DECEXP(SP), R0	: Initially, positive offset
06 13 0247 657	BEQL FLOAT	: Get decimal exponent
52 14 CE 0248 660	BGTR 20\$: If zero, we're done
50 50 CE 024E 661	MNEGL #20, R2	: Positive?
10 50 D1 0251 662	MNEGL R0, R0	: No, use negative offset
03 50 0B 15 0254 663	CMPL R0, #16	: Absolute value
F9 57 E0 0256 664	BLEQ 50\$: Within linear table range?
50 57 F9 57 F4 025A 665	BBS R7, R0 40\$: Yes
50 57 0C C1 025D 666	SOBGEQ R7, 30\$: Is the R7th bit of R0 on?
50 57 0C C1 025D 667	ADDL3 #12, R7, R0	: No, try again.
52 00000000'EF42 50 C4 0261 670	MULL2 R0, R2	: This can never fall through.
57 6E 57 00 0264 671	MOVAB OTSSSA CVT TAB[R2], R2	: Index is 12-bit position
57 28 AE 9E 026C 672	MOVL R7, TEMP(SP)	: because table is linear
00000000'EF 57 6E 01 C1 16 0273 673	MOVAB DECEXP+28(SP), R7	: from 0-16.
00000000'EF 57 6E 01 C1 16 0273 674	JSB OTSSCVT MUL	: Get table offset
00000000'EF 57 6E 01 C1 16 0273 675	SUBL3 #1, TEMP(SP), R7	: Table entry address
00000000'EF 57 6E 01 C1 16 0273 676	BGEQ 10\$: Save hi bit position
00000000'EF 57 6E 01 C1 16 0273 677		: This is "common convert routine"
00000000'EF 57 6E 01 C1 16 0273 678		: table base. The +28 offsets
00000000'EF 57 6E 01 C1 16 027D 679		: the -28 location of DEC EXP
00000000'EF 57 6E 01 C1 16 027D 679		: referenced in OTSSCVT_MUL.
00000000'EF 57 6E 01 C1 16 027F 680		: Do the multiplication
00000000'EF 57 6E 01 C1 16 027F 681		: Get next bit position
00000000'EF 57 6E 01 C1 16 027F 682		: Loop back if more
00000000'EF 57 6E 01 C1 16 027F 682		: If we fall through here, then there are no more bits to reduce.
00000000'EF 57 6E 01 C1 16 027F 683		: Test DECEXP to make sure.
00000000'EF 57 6E 01 C1 16 027F 684		: -
00000000'EF 57 6E 01 C1 16 027F 685		
OC AE D5 027F 686	TSTL DECEXP(SP)	: Any bits still on?
05 13 0282 687	BEQL FLOAT	: No, ok
13 19 0284 688	BLSS UNDERFLOW	: Negative, underflow
FF0E 31 0286 689	BRW ERROR	: Yes, exponent too big

0289 691 :+
0289 692 : Create a floating number from the fraction in BINNUM and the
0289 693 : binary exponent in R1. Each datatype has a separate routine
0289 694 : to do this.
0289 695 :-
0289 696
0289 697
0289 698 FLOAT:
02 00 28 AE D5 0289 699 TSTL BINEXP(SP) ; Underflow?
08 19 028C 700 BLSS UNDERFLOW ; Yes
10 AE 8F 028E 701 CASEB DTTYPE(SP), #K_DTTYPE_D, #K_DTTYPE_H
0011' 0293 702 10\$: .WORD FLOAT_D-10\$
0064' 0295 703 .WORD FLOAT_G-10\$
00BD' 0297 704 .WORD FLOAT_H-10\$
0299 705
0299 706 :+
0299 707 : Value underflowed. Check to see if it's allowed. If so, set
0299 708 : value to zero, else error.
0299 709 :-
0299 710
03 04 AE 02 F0 0299 711 UNDERFLOW:
FEE4 31 029E 712 BBS #V_ERR_UFL0, FLAG(SP), 10\$; Allowed?
FEF3 31 02A1 713 BRW ZERO ; Yes
714 10\$: BRW ERROR ; No

FLOAT_G:									
51	56	1C	AE	7D	02F7	744	MOVQ	BINNUM+8(SP), R6	; Restore fraction
28	AE	14	4B	78	02FB	745	ASHL	#20, BINEXP(SP), R1	; Put exponent in proper place
58	56	0B	00	EF	0300	746	BVS	ERROR_G	; Error if overflows
58	58	05	9C	0302	747	EXTZV	#0, #T1, R6, R8	; Extract rounding bits	
56	56	F5	BF	79	030B	748	ROTL	#5, R8, R8	; Left adjust
57	FFE00000	BF	CA	0310	750	ASHQ	#-11, R6, R6	; Shift fraction right 11 places	
57	57	51	CO	0317	751	BICL	#^XFFE00000, R7	; clear possibly shifted bits	
	31	31	1D	031A	752	ADDL	R1, R7	; Add in exponent	
				031C	753	BVS	ERROR_G	; overflow if hidden bit bumps	
0B	04	AE	03	E0	031C	754			; exponent too far
07	58	0F	E1	0321	755	BBS	#V_DONTROUND, FLAG(SP), 15\$; round?	
	56	D6	0325	756	BBC	#15, R8, 15\$; round bit is zero		
57	00	D8	0327	757	INCL	R6	; round		
	C8	1D	032A	758	ADWC	#0, R7			
				032C	759	BVS	ERROR_D	; Error?	
04	04	AE	1B	E1	760	15\$:	BBC	#V_EXT_BITS, FLAG(SP), 17\$	
18	BC	58	B0	0331	761	MOVW	R8, 0ext bits(AP)		
04	04	AE	1F	E1	0335	17\$:	BBC	#V_NEGATIVE FLAG(SP), 20\$; Set sign bit
00	57	1F	E3	033A	762	BBCS	#3T, R7, 20\$; insert sign bit to 1	
52	08	AC	D0	033E	763	MOVL	value(AP), R2	; R2 = reference to result	
82	57	10	9C	0342	764	ROTL	#16, R7, (R2)+	; rotate and store result	
62	56	10	9C	0346	765	ROTL	#16, R6, (R2)		
	0077	31	034A	766	BRW	EX1f		; All done	
			034D	767					
			034D	768					
FE47	31	034D	769	ERROR_G:					
			770	BRW	ERROR			; error return	
			0350	771					

773 FLOAT_H:							
54	14	AE	7D	0350	774	MOVQ	BINNUM+0(SP), R4
56	1C	AE	7D	0350	775	MOVQ	BINNUM+8(SP), R6
51	28	AE	10	0358	776	ASHL	#16, BINEXP(SP), R1
58	54	0F	69	035D	777	BVS	ERROR_H
58	58	00	EF	035F	778	EXTZV	#0, #T5, R4, R8
50	56	0F	01	0364	779	ROTL	#1, R8, R8
54	54	F1	8F	0368	780	EXTZV	#0, #15, R6, R0
56	56	F1	8F	036D	781	ASHQ	#-15, R4, R4
55	0F	11	50	0372	782	ASHQ	#-15, R6, R6
57	FFFE0000	BF	CA	0377	783	INSV	RO, #17, #15, RS
	57	51	CO	037C	784	BICL	#^XFFE0000, R7
		40	1D	0383	785	ADDL	R1, R7
				0386	786	BVS	ERROR_H
11	04	AE	03	0388	787		
0D	58	0F	E0	0388	788	BBS	#V_DONTROUND, FLAG(SP), 158
			E1	038D	789	BBC	#15, R8, 158
	54	D6	0391	790		INCL	R4
	55	00	D8	0393	791	ADWC	#0, R5
	56	00	D8	0396	792	ADWC	#0, R6
	57	00	D8	0399	793	ADWC	#0, R7
		2A	1D	039C	794	BVS	ERROR_H
04	04	AE	1B	E1	795	158:	BBC
18	BC	58	B0	03A3	796	MOVW	#V_EXT_BITS, FLAG(SP), 178
04	04	AE	1F	E1	797	178:	BBC
00	57	1F	E3	03A7	798	BBCS	#V_NEGATIVE, FLAG(SP), 208
52	08	AC	D0	03B0	799	208:	MOVL
82	57	10	9C	03B4	800	ROTL	#3T, R7, 208
82	56	10	9C	03B8	801	ROTL	value(AP), R2
82	55	10	9C	03BC	802	ROTL	#16, R7, (R2)+
62	54	10	9C	03C0	803	ROTL	#16, R6, (R2)+
				03C4	804	ROTL	#16, R5, (R2)+
				03C4	805	ROTL	#16, R4, (R2)
				03C4	806		
				03C4	807		
				03C4	808		
				03C4	809		
				03C4	810	EXIT:	
50	01	D0	03C4	811		MOVL	#1, R0
	04	04	03C7	812		RET	
			03C8	813			
			03C8	814	ERROR_H:		
FDCC	31	03C8	815			BRW	ERROR
		03CB	816				

					.SBTTL RGET - get next character
					Subroutine RGET
					Input:
					R0 = number of characters remaining in string
					R1 = address of current character
					Output:
					R0 is decremented by 1. If R0 is now non-positive, RGET returns immediately, indicating that the end of the string has been reached.
					If there is string remaining, R1 now points to the new current character, and R3 has that character.
					If V_SKIPBLANKS is set in caller_flags, blanks are ignored, otherwise a blank is converted to '0'.
					If V_SKIPTABS is set, tabs are ignored.
					RGET:
					DECL R0 ; decrement length counter
					BLEQ 20\$; If string empty, return
					INCL R1 ; R1 points to new character
					MOVZBL (R1), R3 ; R3 gets character
					#V_SKIPTABS, FLAG+4(SP), 10\$; Not skipping tabs? BBC ; FLAG is offset by 4 to allow for JSB to RGET.
					10\$: CMPL R3, #9 ; Is it a tab? BEQL RGET ; Yes
					CMPL R3, #^A/ / ; is character a blank? BNEQ 20\$; return if not
					BBS #V_SKIPBLANKS, FLAG+4(SP), RGET ; if it is a blank, and V_SKIPBLANKS is set, ignore this character. FLAG must be offset by 4 to adjust for the JSB to RGET.
					MOVBL #^A/0/, R3 ; set R3 to zero
					RSB ; return

03EC 860 .SBTTL MUL10_R9 - multiply FAC by 10 and add digit in R3

03EC 861

03EC 862 :+ Subroutine MUL10_R9

03EC 863 : Input:
03EC 864 : R4-R7 - FAC

03EC 865 : R9 - count of decimal digits currently held in FAC

03EC 866 : Output:
03EC 867 : R4-R7 - FAC*10 + digit in R3

03EC 868 : R9 - updated count

03EC 869

03EC 870 :-

03EC 871

SC 59 09 F3 03EC 872 MUL10_R9:
12 59 D1 03F0 873 AOBLEQ #9, R9 M1 : If 9 or fewer digits, use M1.
40 15 03F3 874 CMPL R9, #18 : If 18 or fewer digits,
03F5 875 BLEQ M2 : use M2.

03F5 876 :+ Process entire octaword (four longwords), since there are > 18 digits.

03F5 877 :-

50 55 01 50 DD 03F5 878 M4: PUSHL R0 : Free up a scratch register.
56 56 01 1F FF 03F7 880 EXTZV #31, #1, R5, R0 : Save bit that will be lost.
56 56 50 C0 0400 881 ASHQ #1, R6, R6 : Multiply high part by 2.
54 54 01 79 0403 882 ADDL R0, R6 : Replace bit lost in shift.
50 55 02 1E EF 0407 883 ASHQ #1, R4, R4 : Multiply low part by 2.
7E 56 02 79 040C 884 EXTZV #30, #2, R5, R0 : Save bits that will be lost.
6E 56 50 C0 0410 885 ASHQ #2, R6, -(SP) : Multiply high part by 4.
7E 54 02 79 0413 886 ADDL R0, (SP) : Replace bits lost in shift.
54 8E C0 0417 887 ASHQ #2, R4, -(SP) : Multiply low part by 4.
55 8E D8 041A 888 ADDL (SP)+, R4 : Add 8*FAC to 2*FAC.
56 8E D8 041D 889 ADWC (SP)+, R5 : ...
57 8E D8 0420 890 ADWC (SP)+, R6 : ...
54 53 C0 0423 891 ADWC (SP)+, R7 : ...
09 1E 0426 892 ADDL R3, R4 : Add digit in R3.
55 00 D8 0428 893 BCC 20\$: If no carry, quit now.
56 00 D8 042B 894 ADWC #0, R5 : ...
57 00 D8 042E 895 ADWC #0, R6 : ...
50 8E D0 0431 896 ADWC #0, R7 : ...
05 0434 897 205: MOVL (SP)+, R0 : Restore scratch register.
0435 898 RSB : Return to caller.

0435 899 :+ Process two low-order longwords only, since there are <= 18 digits.

0435 900 M2: ASHQ #1, R4, R4 : Multiply R4:R5 by 2.
0435 901 :- ASHQ #2, R4, R6 : Multiply R4:R5 by 4.
54 54 01 79 0435 902 ADDL R6, R4 : Add 8*FAC to 2*FAC (low).
54 54 02 79 0439 903 ADWC R7, R5 : Add 8*FAC to 2*FAC (high).
56 56 C0 043D 904 ADDL R3, R4 : Add digit in R3.
55 57 D8 0440 905 ADWC #0, R5 : ...
54 53 C0 0443 906 CLRQ R6 : Restore R6:R7.
55 00 D8 0446 907 ADWC R6 : Return to caller.
56 7C 0449 908 RSB

044C 910 :+ Process low-order longword only, since there are 9 or fewer digits.

044C 911 :-

54 6444 DE 044C 912 M1: MOVAL (R4)[R4], R4 : Multiply R4 by 5.
54 6344 3E 0450 913 MOVAW (R3)[R4], R4 : Multiply R4 by 2 and add R3.
02 12 0454 914 BNEQ 10\$: If nonzero, quit now.
59 D4 0456 915 CLRL R9 : Reset digit count, since digit

OTSSCVTTR
1-011

; Convert text to real (D, G and H) ^{E 14}
MUL10_R9 - multiply FAC by 10 and add 16-SEP-1984 00:31:03 VAX/VMS Macro V04-00
6-SEP-1984 11:13:56 [LIBRTL.SRC]OTSCVTTR.MAR;1 Page 23 (16)

05 0458 917
0458 918 10\$: RSB
0459 919
0459 920 .END
; was not significant.
; Return to caller.

OTS
1-0

BINEXP	= 00000028	OTSSCVT_MUL	***** X 00
BINNUM	= 00000014	OTSSCVT_T_D	0000001E RG 01
CALLER_FLAGS	= 00000014	OTSSCVT_T_G	0000000F RG 01
CHECK_DIGIT	= 000000A7 R 01	OTSSCVT_T_H	00000000 RG 01
COMMON	= 0000002B R 01	OTSS_INPCONERR	***** X 00
CRY	= 00000040	REBASE	0000022D R 01
DECEXP	= 0000000C	REGMASK	= 000003FC
DECIMAL_POINT	= 0000011E R 01	RGET	000003CB R 01
DIGITS	= 00000008	SCALE	00000181 R 01
DIGITS_IN_FRACT	= 0000000C	SCALE_FACTOR	= 00000010
DIGIT_COOP	= 0000009D R 01	TEMP	= 00000000
DTYPE	= 00000010	UNDERFLOW	00000299 R 01
D_EXP	= 000001AB R 01	VALUE	= 00000008
ERROR	= 00000197 R 01	V_DECEXP	= 0000001C
ERROR_D	= 000002F4 R 01	V_DEC POINT	= 0000001E
ERROR_G	= 0000034D R 01	V_DONTROUND	= 00000003
ERROR_H	= 000003CB R 01	V_ERR_UFL0	= 00000002
EXIT	= 000003C4 R 01	V_EXP LETTER	= 00000005
EXPON	= 00000129 R 01	V_EXT_BITS	= 0000001B
EXP_CHECK	= 0000015B R 01	V_FORCESCALE	= 00000006
EXP_COMMON	= 000001BE R 01	V_NEGATIVE	= 0000001F
EXP_DONE	= 00000171 R 01	V_NEG_DECEXP	= 0000001D
EXP_LOOP	= 00000154 R 01	V_ONLY E	= 00000001
EXP_MINUS	= 00000116 R 01	V_SKIPBLANKS	= 00000000
EXP_NEG	= 0000014C R 01	V_SKIPTABS	= 00000004
EXP_PLUS	= 0000010E R 01	ZERO	00000185 R 01
EXT_BITS	= 0000001B	ZERO_VALUE	00000188 R 01
FLAG	= 00000004		
FLOAT	= 00000289 R 01		
FLOAT_D	= 000002A4 R 01		
FLOAT_G	= 000002F7 R 01		
FLOAT_H	= 00000350 R 01		
FOR\$CRV_IN_DEF	= 0000001E RG 01		
FRAME	= 00000050		
G_EXP	= 000001B2 R 01		
H_EXP	= 000001B9 R 01		
INIT_BINEXP	= 000001A0 R 01		
IN_STR	= 00000004		
K_DTYPE_D	= 00000000		
K_DTYPE_G	= 00000001		
K_DTYPE_H	= 00000002		
L_2P31_DIV_10	= 0CCCCCCC		
MT	= 0000044C R 01		
M2	= 00000435 R 01		
M4	= 000003F5 R 01		
MUL10_R9	= 000003EC R 01		
M_DECEXP	= 10000000		
M_DEC POINT	= 40000000		
M_DONTROUND	= 00000008		
M_EXT_BITS	= 08000000		
M_NEG_DECEXP	= 20000000		
NT	= 00000219 R 01		
N2	= 00000203 R 01		
N4	= 000001E9 R 01		
NOT_DIGIT	= 000000C9 R 01		
NO_OF_FLAGS	= 00000007		
OTSSA_CVT_TAB	= ***** X 00		

+-----+
! Psect synopsis !
+-----+

PSECT name

Allocation PSECT No. Attributes

00000000 (0.) 00 (0.) NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
00000459 (1113.) 01 (1.) PIC USR CON REL LCL SHR EXE RD NOWRT NOVEC LONG

! Performance indicators !

Phase

Page faults CPU Time Elapsed Time

Initialization	32	00:00:00.05	00:00:02.00
Command processing	123	00:00:00.31	00:00:03.29
Pass 1	102	00:00:01.46	00:00:05.28
Symbol table sort	0	00:00:00.05	00:00:00.05
Pass 2	163	00:00:01.07	00:00:05.44
Symbol table output	8	00:00:00.07	00:00:01.42
Psect synopsis output	2	00:00:00.01	00:00:00.01
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	432	00:00:03.02	00:00:17.49

The working set limit was 1200 pages.

16746 bytes (33 pages) of virtual memory were used to buffer the intermediate code.

There were 10 pages of symbol table space allocated to hold 87 non-local and 37 local symbols.

920 source lines were read in Pass 1, producing 19 object records in Pass 2.

0 pages of virtual memory were used to define 0 macros.

Macro library statistics

Macro Library name

Macros defined

\$255\$DUA28:[SYSLIB]STARLET.MLB:2

0

0 GETS were required to define 0 macros.

There were no errors, warnings or information messages.

MACRO/ENABLE=SUPPRESSION/DISABLE=(GLOBAL,TRACEBACK)/LIS=LIS\$:OTSCVTTR/OBJ=OBJ\$:OTSCVTTR MSRC\$:OTSCVTTR/UPDATE=(ENH\$:OTSCVTTR)

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VAX/VMS V4.0

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